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P2002,0634

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By: Phung Chung Date: February 2, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.	:	10/631,356	Confirmation No.	3462
Applicant	:	Sven Boldt		
Filed	:	July 31, 2003		
TC/A.U.	:	2138		
Examiner	:	Phung Chung		
Title:	:	Method for Testing a Semiconductor Memory having a Plurality of Memory Banks		
Docket No.	:	P2002,0634		
Customer No.	:	24131		

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Office action dated November 2, 2005,
please amend the above-identified application as follows:

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 5 of this paper.

Applic. No.: 10/631,356
Amdt. Dated February 2, 2006
Reply to Office action of November 2, 2005

Amendments to the Drawings begin on page 9 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 10 of this paper.

An Appendix including amended drawing figures is attached following page 14 of this paper.